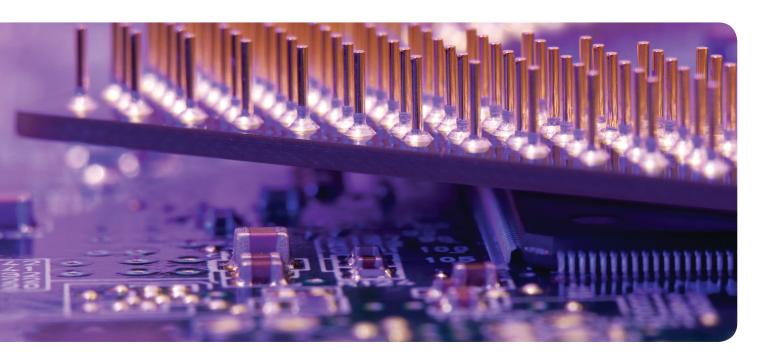
unec



# UNLOCKING THE POWER OF THE GPU

New programming language Quasar cuts development timelines for parallel computing algorithms from months to a matter of days



# QUASAR AT A GLANCE

### WHAT IT DOES

Accelerating image reconstruction and video processing by 10 to 100 times through faster, easier programming of GPU/CPU parallel computing

#### WHERE IT CAN BE APPLIED

- Medical imaging and data processing
- Scientific software development
- Real-time video processing
- Automotive applications

When used in parallel with the central processing unit (CPU), graphics processing units (GPUs) can improve computation speed by up to 100 times—but this is rarely done because GPUs are notoriously hard to program. The new Quasar language changes that. Created by iMinds researchers at Ghent University, Quasar opens up whole new possibilities for faster GPU programming and processing in medical imaging reconstruction, video processing and more.

#### THE CHALLENGE

#### Harness GPU potential efficiently

Developing a program that uses both the CPU and GPU requires programmers to master multiple specialized, platform-specific coding languages, which can take months of dedicated training. Development then takes several more months. Even then, a program written for one GPU is not always portable to another (or even others made by the same manufacturer)—forcing programmers to rewrite code whenever a new GPU is released. For all these reasons, making use of the GPU's computation resources has not been cost-effective enough for most companies to consider... until now.

#### THE BREAKTHROUGH

# A simpler way to develop universal, future-proof GPU code

An easy-to-learn, high-level programming language that's hardware-independent, Quasar automates and streamlines GPU algorithm development and programming—making it faster and easier to leverage GPUs for parallel computing. A compiler takes the code written in Quasar's integrat-

# uniec



ed development environment and automatically translates it for use with specific GPUs. A patent-protected runtime suite then determines the optimal balance of program execution between the GPU and CPU.

The result? Code that can be developed, tested and run more quickly and efficiently—and then seamlessly re-used with different GPUs today and tomorrow.

#### THE OPPORTUNITY

Faster development, better performance, minimal effort

Quasar is a valuable addition to the programming arsenal of not only scientific software and digital media developers but also those working in the medical imaging sector, where parallel computing can accelerate image reconstruction and video processing by 10 to 100 times thanks to faster computations during program execution.

With Quasar, developers need to learn just one simple programming language to code for all different types of GPUs, greatly reducing the amount of training required. And because it is so easy to use, Quasar dramatically cuts development timelines. In one test, experts using CUDA/C++ took three months to implement an MRI reconstruction algorithm; a developer using Quasar for the very first time achieved the same numerical results at the same computational performance in just three days.

While other programming languages support GPUs—indirectly like MATLAB and Mathematica, or directly like CUDA and OpenCL—none offers the user-friendly integrated development environment, faster development and execution times, hardware independence and future-proof code made possible with Quasar.

Prof. Bart Goossens imec - Ghent University

## **HOW QUASAR WORKS**

Quasar features an integrated development environment called Redshift that is easy to learn, use and master. Quasar optimizes Redshift's high-level code through two separate, inter-related components:

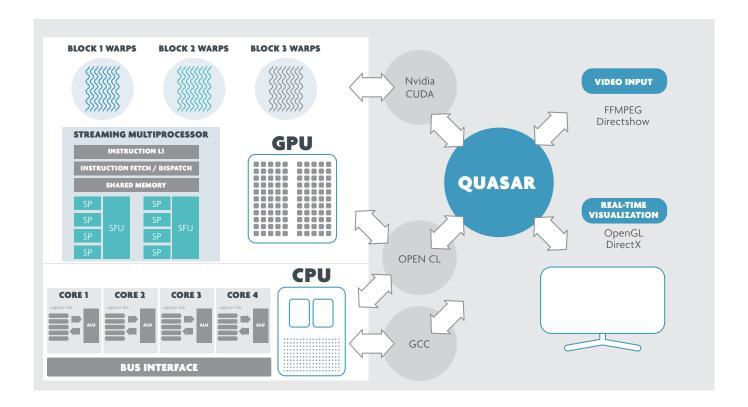
- **The compiler**, which translates the initial hardware-independent code so it can be used with GPU models of your choosing. With a modular approach that makes it easy to optimize for new GPUs as they are released, Quasar helps maintain algorithm performance and functionality over the long term—without having to rewrite the original code.
- The runtime suite, which looks at current load and other factors to determine which parts of code should run off the CPU and GPU for optimal program execution. The runtime suite also automatically handles many tasks programmers typically need to do manually, including scheduling, load balancing, and memory transfer and management.

In one test, experts using CUDA/C++ took three months to implement an MRI reconstruction algorithm; a developer using Quasar for the very first time achieved the same numerical results at the same computational performance in just three days.

99

umec

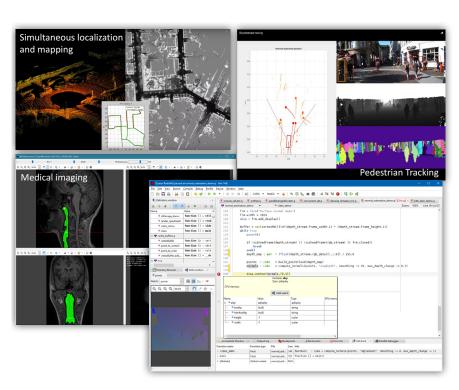




These tools enable rapid prototyping, making it possible to test several different combinations of algorithms and parameters within minutes. The results can then be deployed right away without having to be exported to another programming language.

While automation is at the heart of Quasar, it also provides options for programmers who prefer a more hands-on approach. Users can, for example, add extra code to tell the compiler how to transfer memory blocks, or even generate C++ or CUDA code to be further modified and integrated into existing code bases.







#### **ABOUT IMEC**

Imec is the world-leading research and innovation hub in nanoelectronics and digital technologies. The combination of our widely acclaimed leadership in microchip technology and profound software and ICT expertise is what makes us unique. By leveraging our world-class infrastructure and local and global ecosystem of partners across a multitude of industries, we create groundbreaking innovation in application domains such as healthcare, smart cities and mobility, logistics and manufacturing, energy and education.

#### **AMERICAS**

raffaella.borzi@imec.be T +1 408 386 8357

### JAPAN

isao.kawata@imec.be T +81 90 9367 8463

#### **CHINA**

timo.dong@imec-cn.cn +86 13564515130

### TAIWAN & SE-ASIA

mavis.ho@imec.be T +886 989 837 678

#### **EUROPE & ISRAEL**

michel.windal@imec.be +32 478 96 67 29

#### VIETNAM, BRAZIL, RUSSIA, MID EAST, INDIA

max.mirgoli@imec.be T +1 415 480 4519

DISCLAIMER - This information is provided 'AS IS', without any representation or warranty. Imec is a registered trademark for the activities of IMEC International (a legal entity set up under Belgian law as a "stichting van openbaar nut"), imec Belgium (IMEC vzw supported by the Flemish Government), imec the Netherlands (Stichting IMEC Nederland, part of Holst Centre which is supported by the Dutch Government), imec Taiwan (IMEC Taiwan Co.) and imec China (IMEC Microelectronics (Shanghai) Co. Ltd.) and imec India (Imec India Private Limited), imec Florida (IMEC USA nanoelectronics design center).